@INPROCEEDINGS{1250315,
author={Viana, P. and Barros, E. and Rigo, S. and Azevedo, R. and
Araujo, G.},
booktitle={15th Symposium on Computer Architecture and High
Performance Computing, 2003},
title={Exploring memory hierarchy with ArchC},
year={2003},
month=nov.,
volume={},
number={},
pages={2 - 9},
keywords={ArchC language; SPARC-V8 processor-based architecture;
SystemC model; architecture description language; cache
configuration exploration; design space exploration; image
processing application; memory hierarchy; processor architecture;
programmable system; cache storage; hardware description languages;
memory architecture; system-on-chip;},
doi={10.1109/CAHPC.2003.1250315},
ISSN={},}